

# Lawrence Livermore Laboratory

COMMENTS ON THE LAPP-POWERS "COMPUTER-AIDED SYNTHESIS OF FAULT TREES"

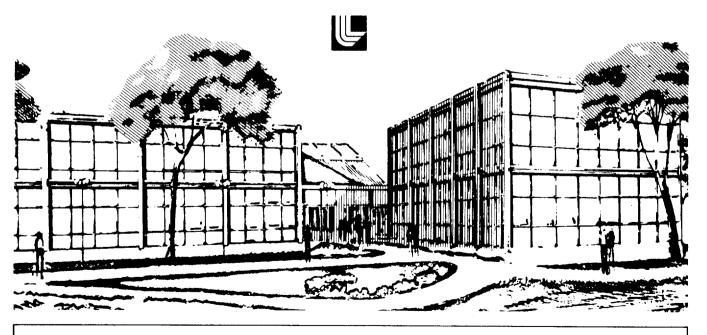
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# Comments on the Lapp-Powers "Computer-aided Synthesis of Fault Trees"

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Keywords - - Fault Tree Synthesis, Computer Aid

Reader Aids -

Purpose: Comment on the fault tree synthesis algorithm and its use

Special Math Needed: Fault Tree Logic

Results useful to: Reliability analysts, Fault-tree researchers

Summary: A simplified algorithm for constructing fault trees from digraphs for negative feedback loops is presented. The use of the Lapp-Powers fault tree synthesis algorithm in nuclear material safeguards assessment is described. Advantages of using digraphs to construct fault trees are also described.

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I believe that the Lapp Powers (LP) approach would become simpler and more understandable if they change their fault tree synthesis algorithm [1,2] by --

- 1) Modeling noise in the negative feedback loop operator,
- 2) Dropping use of the exclusive OR (XOR) operator.

The XOR operator is unnecessary from an engineering viewpoint. The XOR operator generates complemented events in the prime implicants that are normally true. A cardinal rule of fault tree construction I learned from David Haasl (DH) [3] is:

"Expect no miracles; those things that normally occur as the result of a fault will occur, and only those things. Also, normal system operation may be expected to occur when faults occur."

LP used their rule in their algorithm. As an example, if a given disturbance exists which will cause the top event to occur, then LP assume that randomly occurring disturbances of the opposite sign will not cancel the given disturbance.

Using this rule, events which reverse gains and in turn cause the top-event variable to deviate opposite to the specified direction are not considered (because these events are not the result of normal operation).

For example, consider event 7 of the fault tree in [2, Fig. 5]. According to the above rule, event 7 is developed as

Control loop causes or passes disturbances decreasing M8

0R

Valve 5 reversed

<sup>&#</sup>x27;Low air pressure on the cooling water control valve (P7(-1))' AND'valve 5 not reversed'

The event "valve 5 not reversed" is normally true and is deleted when developing the fault tree. In most cases, when one uses DH's rule, there will be no complemented events in the fault tree and the fault tree is <u>s</u>-coherent. When one finds the min cut sets using DH's rule, some of the min cut sets can be mutally exclusive from a physical viewpoint, i.e., the intersection of two min cut sets might not cause the top event. This is because normally-true conditions are dropped that would make the min cut sets (really the implicants now) mutually exclusive.

When one employs the LP synthesis algorithm using DH's rule, mutually exclusive events are not generated in the same min cut set due to the consistency checks LP employs for negative feedback loops (NFBLs) and negative feedforward loops.

Fig. 1 gives simplified NFBL operator for constructing fault trees from digraphs. The important point about this operator is that it considers at once all external disturbances entering the loop. The operator has to be considered only once when developing the fault-tree logic for a deviation of a variable on a NFBL. LP use their NFBL loop operator several times when tracing the cause and effect around the loop. The sign of the external disturbances in fig. 1 is determined by examining the system digraph and establishing the net normal system-gain from the disturbance to the loop variable under development in the fault tree.

The simplified operator gives special attention to the location where moderate disturbances enter the NFBL, (see figs. 1 and 2). Moderate disturbances are by definition those which the NFBL is able to cancel. For a moderate disturbance entering a NFBL to cause a deviation of a variable on the NFBL, the following conditions must be met (refer to fig. 2).

- 1. No control devices are inactivated from the point the disturbance enters the loop downstream to the loop variable under development (the term <u>downstream</u> means in the same direction as the arrows are pointing in the digraph).
- 2. At least one control device is inactivated on the remainder of the loop.

Condition 1 permits the disturbance to propagate down the loop. Condition 2 inactivates the loop so that no corrective action from the NFBL is possible.

In the simplified algorithm, reversal of gains is not considered when external disturbances enter the loop. This is because a reversal event is sufficient to fail the NFBL. In the LP synthesis algorithm, this effect is considered; however the cut sets that are generated by the LP synthesis algorithm are not minimal. When considering how NFBLs can fail when external disturbances enter, (see the two right-hand inputs in fig. 1) the simplified algorithm and the LP synthesis algorithm generate identical min cut sets provided that the use of the XOR operator is dropped in the LP algorithm.

The use of the simplified operator for the event, High Nitric Acid Temperature from Heat Exchanger T3(+1) (see system digraph in [2, Fig. 3,]) is given in fig. 3. The identification number for each basic event is shown on the fault tree in fig. 3. There are 19 min cut sets as listed below

- 1. 3 7. 1,11 13. 2,12 19. 7,9
- 2. 10 8. 1,12 14. 2,13
- 3. 14 9. 1,13 15. 2,17
- 4. 15 10. 1,17 16. 4,18
- 5. 16 11. 2,5 17. 6,18
- 6. 1,5 12. 2,11 18. 7,8

Two types of failure were described for control devices on NFBLs [2]:

- Inactivation of components causing zero gains on the NFBL,
   eg. controller-broken or sensor-broken.
- Reversal of components causing a reversal of gains on the NFBL, eq. reversed-valve-action or controller-action-reversed.

There is a third type of failure, not explicitly described in [2], that is modeled as a large input disturbance to the NFBL. This type of failure involves control devices on the NFBL failing high or low and is sufficient to cause a deviation in a loop variable on the NFBL. For example, failure that cause T3(+1) in [2] are 1) valve 5 fails closed, 2) controller set point low, 3) controller fails low, 4) temperature sensor fails low, 5) instrument air line rupture. These failures are modeled as large input disturbances in the simplified NFBL loop operator in fig.1.

I draw the following conclusions about the simplified operator:

- 1. When reversal events occur that cause the NFBL loop to be positive, noise appears as a cause of system failure (eg. see min cut sets 16 and 17).
- 2. Complemented events (ie component successes) do not appear in the min cut sets.
- 3. For external disturbances entering the NFBL, the simplified operator and the LP synthesis algorithm generate the identical min cut sets provided that the use of the XOR operator is dropped in the LP synthesis algorithm.
- 4. The simplified operator can be used to describe failure of a simple NFBL and cannot be used to describe failure of 1) nested NFBLs or 2) multiple NFBLs affecting the same variable.

5. I found through my experience in teaching fault tree courses that course participants readily understand the relationale behind the logic of the simplified operator. They find it an easy process of searching the NFBL on the digraph and providing the inputs to the fault tree shown in fig. 1. Unfortunately, they found LP's synthesis algorithm for NFBLs much more difficult to comprehend and use.

# Demonstration of the Use of Lapp-Powers Fault Tree Synthesis Algorithm.

Lawrence Livermore Laboratory (LLL) is developing an assessment procedure for the U.S. Nuclear Regulatory Commission [4,5]. The purpose of the procedure is to assess the effectiveness of a potential licensee's material-control system--a system used to protect against theft of special nuclear material (SNM) such as plutonium or uranium 235. An important step in the assessment procedure includes generating adversary sets--the sets of conditions and adversary acts necessary for successful theft of SNM. The study has demonstrated [6] for a complex prototype material-control system that these event sets can be generated from min cut sets of fault trees with top event "successful diversion of SNM." These fault trees were systematically obtained from a system digraph by the use of the LP fault tree synthesis algorithm. The corrective actions of the material control system were modeled by NFBLs and negative feedforward loops. For successful theft of SNM to occur, all the loops on the system digraph must fail.

These loops fail as the result of:

- 1. Random monitor failure
- 2. Monitor measurement sensitivity inadequate
- 3. Human error, including slow guard response
- 4. Adversary activity, including equipment tampering and collusion.

These 4 events were represented as zero-gain events on the system digraph.

As suggested by Lapp and Powers [7], the loops were found in the system digraph and classified according to their range and dynamics. The corrective action of some loops were so slow, that these loops were failed prior to applying the algorithm. The advantage to this approach is efficiency. One does not need to consider all the combinations of events listed above that are necessary in failing loops. Hence the resulting fault tree is smaller in size.

I feel that one major advantage of using digraphs to construct fault trees is that (1) the cause-and-effect relationships existing between process and state variables and (2) the dynamics of the relationships can be readily displayed in the system digraph. In most other techniques, these relationships must be inferred from the system schematic and event descriptions in the fault tree. Hence, I feel that fault trees generated from digraphs give more information than fault trees generated from any other technique. An important aspect of fault tree analysis is the ability to display the analysis to others.

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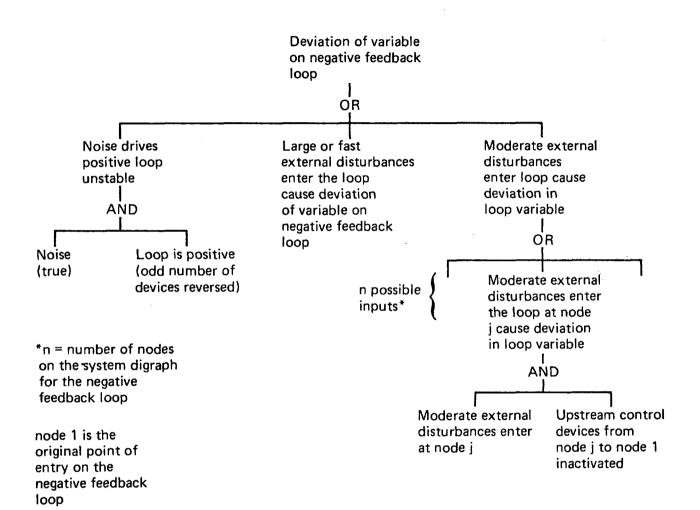


Fig. 1 Simplified Negative Feedback Loop Operator

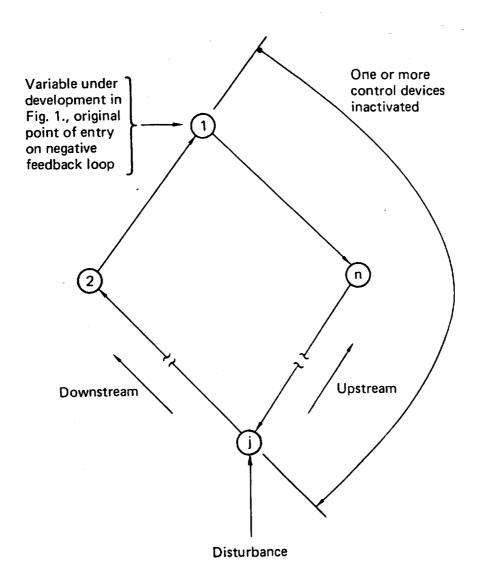


Fig. 2 Failure of NFBL for External Moderate Disturbances

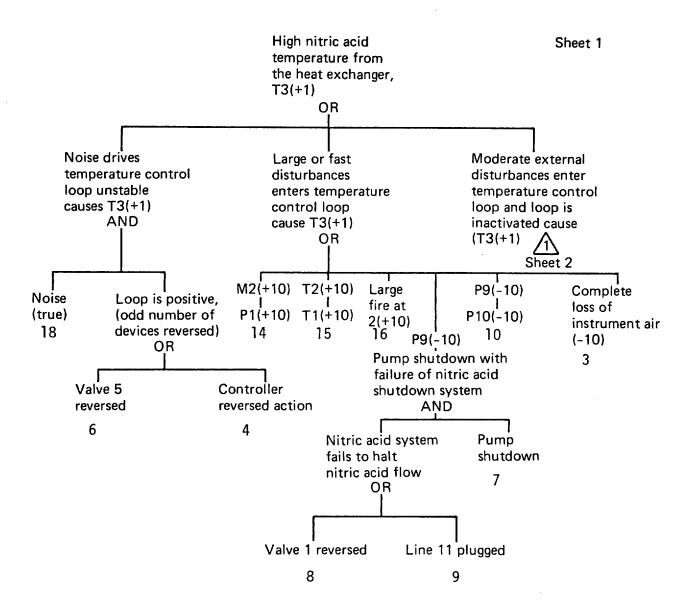


Fig. 3 Fault Tree Generated From Simplified NFBL Operator

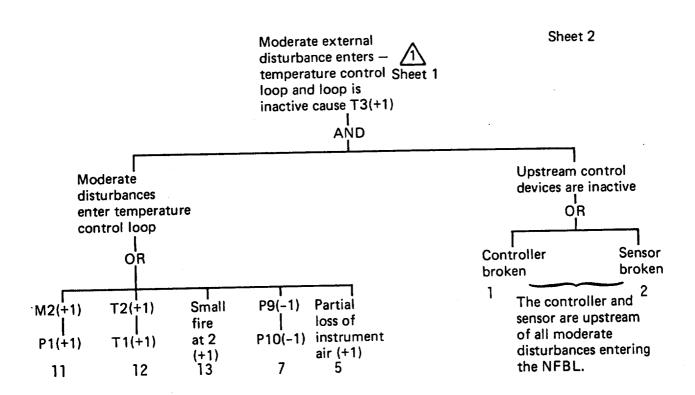


Fig. 3 Continued

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<sup>\*</sup>Available from National Technical Information Servcie; Springfield, VA. 22151, USA